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Electronic Embedded System Design - Important Feature of Online Electronics Magazines by Shubhranshu Agarwal

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The world of electronics is witnessing a revolution in the way products are conceived, designed and implemented. The ever growing importance of the web, the advent of microprocessors of great computational power, the explosion of wireless communication, the development of new generations of integrated sensors and actuators are changing the world in which we live to perform. The online electronic magazines provide every information about the ongoing trends that take place in the designs of electronics including those in Electronic embedded system design. One well designed and planned magazine that I like much is Electronics For You; it provides every possible knowledge about ongoing trends in electronic designs. It is a boon for all those associated with electronics trade.

Electronic embedded system design is a routine segment of this online electronics magazine. The overall goal of electronic embedded system design is to balance production costs with development time and cost in view of performance and functionality considerations. Manufacturing cost depends mainly on the hardware components of the product. Minimizing production cost is the result of a balance between competing criteria. If one considers an integrated circuit implementation, the size of the chip is an important factor in determining production cost. Minimizing the size of the chip implies tailoring the hardware architecture to the functionality of the product.

As the complexity of the products under design increases, the development efforts increase dramatically. At the same time, the market dynamics for electronics systems push for shorter and shorter development times. It will be soon imperative to keep to a strict design time budget, no matter how complex the design problem, with as little as six months from initial specification to a final and correct implementation. To keep these efforts in check and at the same time meet the design time requirements a design methodology that favors reuse and early error detection is essential. We expect the majority of high volume platforms developed to be programmable either at the logic/interconnect level (e.g. via FPGA) or using software.

Both reuse and early error detection imply that the design activity must be defined well, so that all phases are clearly identified and appropriate checks are enforced. To be effective, Electronic embedded system design methodology that addresses complex systems must start at high levels of abstraction. The professionals of most companies, engaged in Electronic embedded system designs and IC design, are familiar with working at levels of abstraction that are too close to implementation.

It is must to know the latest updates in Electronic embedded system design practices to keep the production cost and the product cost bare minimum; and efy like electronics web-portals may be the good source to get all the information within seconds.

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